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Application for Letters Patent

**TITLE:** SEMICONDUCTOR INTEGRATED CIRCUIT CARD MANUFACTURING  
METHOD, AND SEMICONDUCTOR INTEGRATED CIRCUIT CARD

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SEMICONDUCTOR INTEGRATED CIRCUIT CARD MANUFACTURING METHOD, AND  
SEMICONDUCTOR INTEGRATED CIRCUIT CARD

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a semiconductor integrated circuit card manufacturing method and a semiconductor integrated circuit card.

Description of the Related Art

A semiconductor integrated circuit chip (IC chip) charged in a semiconductor integrated card (IC card) is sealed by the screen printing of resin.

The IC chip charged in such an IC card has a drawback that the strength thereof is low and dispersion of seal shape and seal thickness thereof is large because it is sealed by the screen printing of resin. If the strength of the IC chip is low, there is a fear that the IC chip is damaged or the connection of the electrodes of the IC chip to the circuit pattern of a substrate comes off when the IC card is bent by an external pressure. If the dispersion of the seal shape and the seal thickness of the IC chip is large, it becomes difficult to control the quality and yield of the IC card.

SUMMARY OF THE INVENTION

In view of such a point, an object of the present invention is to propose a semiconductor integrated circuit chip

seal method and a semiconductor integrated circuit chip device capable of providing a semiconductor integrated circuit chip device having extremely high strength and less dispersion of seal shape and seal thickness.

Furthermore, the present invention is about to propose such a semiconductor integrated circuit card that there is no fear that a semiconductor integrated circuit chip device is damaged or connection of the electrode thereof to the circuit pattern of a substrate comes off, and control of quality and yield thereof is easy.

A method for manufacturing a semiconductor integrated circuit card according to a first present invention includes the steps of bonding a semiconductor integrated circuit chip onto a substrate having a circuit pattern formed thereon, connecting an electrode of the semiconductor integrated circuit chip to the circuit pattern, and applying a predetermined quantity of seal resin onto a first reinforcement metal plate; placing the substrate having the semiconductor integrated circuit chip bonded thereto on the first reinforcement metal plate via the applied seal resin; applying a predetermined quantity of seal resin onto the semiconductor integrated circuit chip; disposing a second reinforcement metal plate above the applied seal resin, pressurizing the seal resin via the first and second reinforcement metal plates, and letting flow the seal resin along a peripheral face of the semiconductor integrated circuit

chip; and hardening the seal resin let flow along the peripheral face of the semiconductor integrated circuit chip.

According to a method for manufacturing a semiconductor integrated circuit card of the second present invention, a semiconductor integrated circuit chip is bonded onto a substrate having a circuit pattern formed thereon, and an electrode of the semiconductor integrated circuit chip is connected to the circuit pattern. In addition, a predetermined quantity of seal resin is applied onto a first reinforcement metal plate. Subsequently, the substrate having the semiconductor integrated circuit chip bonded thereto is placed on the first reinforcement metal plate via the applied seal resin. Subsequently, a predetermined quantity of seal resin is applied onto the semiconductor integrated circuit chip. Subsequently, a second reinforcement metal plate is disposed above the applied seal resin. In addition, the seal resin is pressurized via the first and second reinforcement metal plates, and the seal resin is let flow along a peripheral face of the semiconductor integrated circuit chip, and the seal resin let flow along the peripheral face of the semiconductor integrated circuit chip is hardened.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIGS. 1A and 1B are each a process diagram showing an essential part of a semiconductor integrated circuit card according to a first embodiment of the present invention;

FIGS. 2A to 2E are each a process diagram showing a seal

method for an IC chip of the first embodiment;

FIG. 3 is a cross-sectional view showing an IC card by the first embodiment;

FIGS. 4A and 4B are each a process diagram showing an essential part of a semiconductor integrated circuit card according to a second embodiment of the present invention;

FIGS. 5A to 5E are each a process diagram showing a seal method for an IC chip of the second embodiment; and

FIG. 6 is a cross-sectional view showing an IC card by the second embodiment.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereafter, a semiconductor integrated circuit chip (IC chip) seal method, a semiconductor integrated circuit chip device (IC chip device), and a semiconductor integrated circuit card (IC card), which are concrete examples of the invention, will be described with reference to the drawings.

First of all, with reference to FIGS. 1A, 1B and FIGS. 2A to 2E, an IC chip seal method and an IC chip device (FIG. 1B), which are concrete examples of the present invention, will be described. As shown in FIG. 2A, a film substrate 101 having a plurality of circuit patterns KP formed thereon is prepared. As shown in FIG. 2B, a small quantity of bonding agent BD1 is applied onto each circuit pattern KP of the film substrate 101 or onto the film substrate 101 in each circuit pattern KP of FIG. 2A. As shown in FIG. 2C, an IC chip 102 is placed on the

bonding agent BD1 of each circuit pattern KP of the film substrate 101, and each IC chip 102 is pressurized. In addition, each IC chip 102 and the film substrate 101 are heated, and each IC chip 102 is bonded (stuck) to a predetermined portion of each circuit pattern KP of the film substrate 101. In addition, an electrode of each IC chip 102 is connected to a predetermined portion of the corresponding circuit pattern KP.

And as shown in FIG. 2D and FIG. 1A, a predetermined quantity of seal resin 103 (bonding agent BD2) is applied onto the IC chip CP, and the seal resin 103 (bonding agent BD2) is irradiated with ultraviolet rays to be subjected to a temporary hardening. As shown in FIG. 2E, FIG. 1A and FIG. 1B, a (rectangular) reinforcement metal plate 104 having a figure similar to the top face (rectangular) of the IC chip 102 and having a wider area than the top face of the IC chip 102 is disposed above the temporarily hardened seal resin 103 (bonding agent BD2). By using a pressurizing jig, the reinforcement metal plate 104 is uniformly pressurized against the top face of the IC chip 102 while holding the parallel state between the reinforcement metal plate 104 and the IC chip 102. As shown in FIG. 1B, the seal thickness is thus made constant. In addition, the seal resin 103 (bonding agent BD2) is let flow along the peripheral face of the IC chip 102 to bond the reinforcement metal plate 104 to the top face of the IC chip 102 and cover the

peripheral face of the IC chip 102. Then, a seal resin portion 105 (bonding agent BD2) is irradiated with the ultraviolet rays and thereby hardened. As a result, a plurality of IC chip devices CPD each having a fixed shape and having a fixed seal thickness as shown in FIG. 1B are obtained.

Although not illustrated, the plurality of IC chip devices CPD are punched out at every circuit pattern KP, and are placed on a roll film. In addition, a thin film coating is conducted over each of the IC chip devices CPD and the roll film. Thereafter, the IC chip devices CPD and the roll film are passed between heating rollers. As a result, a card coupling plate having a number of IC cards coupled in a line is obtained. This card coupling plate is cut at a fixed interval. A plurality of fixed length card coupling plates each having a predetermined number of IC cards coupled in a line are obtained. The fixed length card coupling plate is punched out at every IC card. Individual IC cards are thus obtained.

The structure of the IC card (semiconductor integrated circuit card) having an IC chip enclosed therein will now be described by referring to FIG. 3. The IC chip device CPD is mounted in a card substrate. In other words, the IC chip device CPD is charged into a filled resin layer 106 made of, for example, epoxy resin. Armoring resin plates 107 and 108 made of, for example, polyethylene terephthalate are bonded to the front and rear of the filled resin layer 106, respectively,

whereby an IC card is formed.

Therefore, according to the manufacturing method described above, it is possible to obtain such a semiconductor integrated circuit chip device in which the semiconductor integrated circuit chip, the reinforcement metal plate, and the seal resin become an integral structure, the strength is high, and the seal shape and the seal thickness have less dispersion. Therefore, such a semiconductor integrated circuit chip device is extremely suited to be mounted on a semiconductor integrated circuit card.

Therefore, it is possible to obtain such a semiconductor integrated circuit card that there is no fear of a semiconductor integrated circuit chip device being damaged or a connection of an electrode to a circuit pattern of a substrate coming off, and that control of quality and yield is easy.

Next, a semiconductor integrated circuit chip (IC chip) seal method, a semiconductor integrated circuit chip device (IC chip device) and a semiconductor integrated circuit card (IC card), which are concrete examples of the present invention, will be described.

First of all, an IC chip seal method and an IC chip device (FIG. 4B), which are concrete examples of the present invention, will be described with reference to FIGS. 4A, 4B and FIGS. 5A to 5F. As shown in FIG. 5A, a film substrate 401 having a plurality of circuit patterns KP formed thereon is

prepared. As shown in FIG. 5B, a small quantity (fixed quantity) of bonding agent BD1 is applied onto each circuit pattern KP of the film substrate 401 of FIG. 5A or onto the film substrate 401 in each circuit pattern KP by using a dispenser. As shown in FIG. 5C, each IC chip 402 is placed on the bonding agent BD1 of each circuit pattern KP of the film substrate 401, and each IC chip 402 is pressurized. In addition, each IC chip 402 and the film substrate 401 are heated, and each IC chip 402 is bonded (stuck) to a predetermined portion of each circuit pattern KP of the film substrate 401. In addition, the electrode of each IC chip 402 is connected to a predetermined portion of a corresponding circuit pattern KP by using a solder.

As shown in FIG. 4A and FIG. 5D, a plurality of second reinforcement metal plates 405(rectangular) each having a figure similar to the bottom face (rectangular) of the IC chip 402 and having a wider area than the bottom face of the IC chip 402 are provided. Onto each of the first reinforcement metal plates MP1, a predetermined quantity (fixed quantity) of seal resin 403 (bonding agent BD2) is applied by using a dispenser. This process may be conducted before, after, or in the middle of the process described above with reference to FIGS. 5A to 5C.

As shown in FIG. 2E, the film substrate FB having the IC chips 402 bonded thereto is placed on the second reinforcement metal plates 405 via the applied seal resin 403 (bonding agent BD2).

Then, as shown in FIG. 5E and FIG. 4A, a predetermined quantity (fixed quantity) of seal resin 403 (bonding agent BD3) is applied onto each IC chip 402 by using a dispenser, and the seal resin 403 (bonding agent BD3) is irradiated with ultraviolet rays and thus subjected to a temporary hardening. As shown in FIG. 5E, FIG. 4A, and FIG. 4B, a plurality of first reinforcement metal plates 404 (rectangular) each having a figure similar to the top face (rectangular) of each IC chip 402 and having a wider area than the top face of each IC chip 402 are attached to a jig, moved horizontally, and are disposed above the temporarily hardened seal resin 403 (bonding agent BD3). By using a pressurizing jig, a portion between the first and second reinforcement metal plates 404 and 405 is uniformly pressurized, while holding the parallel state among the first and second reinforcement metal plates 404 and 405 and each IC chip 402. As shown in FIG. 4B, the seal thickness is thus made constant, and also the seal resin 403 (bonding agent BD3) is let flow along the peripheral face of each IC chip 402 to bond each second reinforcement metal plate 405 to the film substrate 401 and bond each first reinforcement metal plate 404 onto each IC chip 402 to thereby cover the peripheral face of each IC chip 402. The seal resin 403 (bonding agent BD3) is irradiated with the ultraviolet rays and thereby hardened to form a seal resin portion 406. As a result, a plurality of IC chip devices CPD each having a fixed shape and having a fixed seal thickness as

shown in FIG. 4B are obtained.

Although not illustrated, the plurality of IC chip devices CPD are punched out at every circuit pattern KP, and are placed on a roll film. In addition, thin film coating is conducted over each of the IC chip device CPDs and the roll film. Thereafter, the IC chip device CPDs and the roll film are passed between heating rollers. As a result, a card coupling plate having a number of IC cards coupled in a line is obtained. This card coupling plate is cut at a fixed interval. A plurality of fixed length card coupling plates each having a predetermined number of IC cards coupled in a line are obtained. The fixed length card coupling plate is punched out at every IC card. Individual IC cards are thus obtained.

The structure of the IC card (semiconductor integrated circuit card) having an IC chip 402 enclosed therein will now be described with reference to FIG. 6. The IC chip device CPD is charged in a card substrate. In other words, the IC chip device CPD is charged in a filled resin layer 407 made of, for example, epoxy resin. Armoring resin plates 408 and 409 made of, for example, polyethylene terephthalate are bonded to the front and the rear of the filled resin layer 407, respectively. An IC card is thus formed.

The IC card by the above-mentioned manufacturing method includes a substrate having a circuit pattern formed thereon; a semiconductor integrated circuit chip bonded onto the substrate

and having an electrode connected to the circuit pattern; first and second reinforcement metal plates; and a seal resin portion for covering a peripheral face of the semiconductor integrated circuit chip, sticking the first reinforcement metal plate onto the substrate, and sticking the second reinforcement metal plate onto the semiconductor integrated circuit chip. Therefore, it is possible to obtain such a semiconductor integrated circuit chip device in which the semiconductor integrated circuit chip, the first and second reinforcement metal plates gripping the semiconductor integrated circuit chip interposed between, and the seal resin become an integral structure, so that the strength thereof is extremely high, i.e., durability against an external force, such as bending, load, and shock, applied from both the front and the rear thereof is extremely high, and the seal shape and the seal thickness have less dispersion. Therefore, such a semiconductor integrated circuit chip device is extremely suited to be mounted on a semiconductor integrated circuit card.

According to the third present invention, a semiconductor integrated circuit chip device including a substrate having a circuit pattern formed thereon; a semiconductor integrated circuit chip bonded onto the substrate and having an electrode connected to the circuit pattern; first and second reinforcement metal plates; and a seal resin portion for covering a peripheral face of the semiconductor integrated

circuit chip, sticking the first reinforcement metal plate onto the substrate and sticking the second reinforcement metal plate onto the semiconductor integrated circuit chip, is charged in a card substrate. Therefore, it is possible to obtain such a semiconductor integrated circuit chip device in which

Therefore, it is possible to obtain such a semiconductor integrated circuit card that there is no fear of a semiconductor integrated circuit chip device being damaged or the connection of an electrode to a circuit pattern of a substrate coming off, and that control of quality and yield is easy.

Having described preferred embodiments of the present invention with reference to the accompanying drawings, it is to be understood that the present invention is not limited to the above-mentioned embodiments and that various changes and modifications can be effected therein by one skilled in the art without departing from the spirit or scope of the present invention as defined in the appended claims.